DragonBoard 410c
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BOOT CONFIGURATIONS

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Default Boot Config (0b000) is eMMC on the SDC1

Note 1: Short between resistors pads to force boot from USB when dip-switch is not installed
Note 2: Short between resistors pads to boot from uSD when dip-switch is not installed
PRE-BUCK - 5.0V

PRE-BUCK - 3.7V

V_{out} = 0.6V \times (1 + \frac{R_H}{R_L})

PRE-BUCK 5.0V: R_H = 165k, R_L = 22.1k → 5.08V

PRE-BUCK 3.74V: R_H = 115k, R_L = 22.1k → 3.72V

\text{Idc} = 5A, \text{Isat} = 10A

\text{Pre-Buck Out}

\text{To/From LS EXP CONN}

\text{DCIN_SNS_P}

\text{DCIN_SNS_N}

\text{VPH_PWR_SNS_P}

\text{VPH_PWR_SNS_N}

\text{BATT_ID}

\text{BATT_THERM}

\text{ARM ENERGY PROBE (PWR MEAS)}

\text{VPH_PWR SOURCE}

\text{BOM CONFIG}

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NOTES:

Effective output capacitance
LDOs L1, L2, L3, L4, L8, L9, L11, L17 → Cout_eff = 4.7uF
LDOs L5, L6, L7, L10, L12, L14, L15, L16, L18 → Cout_eff = 1.0uF

PSEUDO CAPLESS LDOs (indicated by # near the capacitor)
L4, L5, L6, L7, L8, L10, L11, L12, L13, L14, L15, L16, L17, L18

(A) (B) (C) (D)
Connect C13, E14, A14, B13 pins to the main ground directly.

CAD Note: Place C87 close to pin G14.

NOTE:
Route two traces as differential and

CAD Note:
Place C98 and C99 close to pin A11 and B11.

CONNECTOR OPTIONS

SUPPORTED

NO

INSTALL GM1

NOTE:
Add shunt capacitor on MICBIAS signal if manufacturer requires it.

NOTE:
Route two traces as differential and

ANALOG MIC
Notes:
1. Current Limit = 55.358 / Radj [komh] = 1.18mA
2. Connect USB ID pin on connector when uAB connector is used
FORCE DSI SELECTION TO DSI2HDMI BRIDGE

FROM APQ

TO HS EXP CONN

TO DSI2HDMI BRIDGE

DSI_SW_SEL_APQ

R100 2K
R101 0

HDMI_HPD_N

R120 100K

(APQ GPIO_20)

(APQ GPIO_32)

54 S6
R38 200

FORCE DSI SELECTION TO DSI2HDMI BRIDGE
ANALOG EXPANSION CONNECTOR

JTAG INTERFACE (TOP)

ANALOG INTERFACES
WCN & WGR Circuitry Shield

Default option: On-board printed antenna

Stuffing option: UFL connector for external antenna
Note:
If 2.4 GHz WLAN spurs are observed around 2.6 to 2.7 GHz replace R97 with 20 nH inductor.
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