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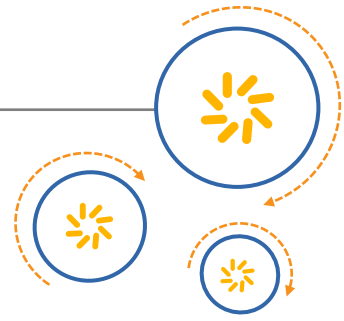

Qualcomm® Snapdragon™ 600E processor

APQ8064E GPIO Pin Assignment Spreadsheet

LM80-P0598-13 Rev. B

September 21, 2016

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Revision history

Revision	Date	Description
B	September 2016	Updated to 'E' part
A	November 2015	Initial release

APQ8064E GPIO map					Alternate functions								
Pad #	**GPIO**	**RPM interrupt**	**Pad**	**Pull (default: options)**	**Alternate function**	**Alternate function**	**Alternate function**	**Alternate function**	**Alternate function**	**Alternate function**	**Alternate function**	**Alternate function**	**Alternate function**
AP4	0		**P3**	B-PD:nppukp		MDP_VSYNC_P	VFE_CAMIF_TIMER6_B						
AP2	1		**P3**	B-PD:nppukp		MDP_VSYNC_S	VFE_CAMIF_TIMER7_B						
B36	2		**P3**	B-PD:nppukp	GP_MN	CAM_MCLK2	VFE_CAMIF_TIMER1_A		BOOT_FROM_ROM				
D38	3		**P3**	B-PD:nppukp	GP_CLK_0A		VFE_CAMIF_TIMER2		WDOG_DISABLE		ETM_TRACE_B_DATA(15)		
C37	4		**P3**	B-PD:nppukp	GP_CLK_1A	CAM_MCLK1	VFE_CAMIF_TIMER3_A		BOOT_CONFIG[6]				
B38	5		**P3**	B-PD:nppukp			CAM_MCLK0		BOOT_CONFIG[5]				
D24	6	►	**P3**	BH-PD:nppukp	GPS_PPS_IN	GPS_PPS_OUT	VFE_CAMIF_TIMER4_C	GSBI1_SPI_CS2A_N	SSBI_TS	GSBI3_3			
E23	7	►	**P3**	BH-PD:nppukp		TS_EOC	GSBI1_SPI_CS3B_N	GSBI1_SPI_CS1A_N		GSBI3_2	ETM_TRACE_B_DATA(14)		
C25	8		**P3**	B-PD:nppukp						GSBI3_1			
E25	9		**P3**	B-PD:nppukp						GSBI3_0			
B26	10	►	**P3**	BH-PD:nppukp			VFE_CAMIF_TIMER6_C	GSBI1_SPI_CS1B_N		GSBI4_3	ETM_TRACE_CTL		
D26	11	►	**P3**	BH-PD:nppukp	MDP_VSYNC_E		VFE_CAMIF_TIMER7_C	GSBI1_SPI_CS2B_N		GSBI4_2	ETM_TRACE_CLK		
B28	12		**P3**	B-PD:nppukp						GSBI4_1	ETM_TRACE_A_DATA(0)		
C29	13		**P3**	B-PD:nppukp						GSBI4_0	ETM_TRACE_A_DATA(1)		
AN5	14		**P3**	B-PD:nppukp		SSBI_FM				GSBI6_3	ETM_TRACE_A_DATA(15)		
AM2	15	►	**P3**	BH-PD:nppukp		SFM_SDI				GSBI6_2	ETM_TRACE_A_DATA(14)		
AM4	16		**P3**	B-PD:nppukp		BT_CTL				GSBI6_1	ETM_TRACE_A_DATA(13)		
AL5	17		**P3**	B-PD:nppukp		BT_DATA_STROBE				GSBI6_0	ETM_TRACE_A_DATA(12)		
D30	18	►	**P3**	BH-PD:nppukp		GP_PDM_0B	VFE_CAMIF_TIMER1_B			GSBI1_3	ETM_TRACE_A_DATA(5)		
E31	19	►	**P3**	BH-PU:nppukp			VFE_CAMIF_TIMER5_B			GSBI1_2	ETM_TRACE_A_DATA(4)		
D32	20		**P3**	B-PD:nppukp						GSBI1_1	ETM_TRACE_A_DATA(3)		
C33	21		**P3**	B-PD:nppukp						GSBI1_0	ETM_TRACE_A_DATA(2)		
B32	22	►	**P3**	BH-PU:nppukp		GP_PDM_1A				GSBI2_3			
E33	23	►	**P3**	BH-PD:nppukp						GSBI2_2			
D34	24		**P3**	B-PD:nppukp						GSBI2_1			
F34	25		**P3**	B-PD:nppukp		GP_CLK_2B				GSBI2_0			
B30	26	►	**P3**	BH-PD:nppdkp		TS_PENIRQ_N							
BB34	27		**P3**	B-PD:nppukp	MI2S_WS								
BD30	28		**P3**	B-PD:nppukp	MI2S_SCLK								
BC31	29	►	**P3**	BH-PD:nppukp	MI2S_DATA_SD3								
BF38	30	►	**P3**	BH-PD:nppukp	MI2S_DATA_SD2		AUD_SB1_DATA_B						
BA33	31		**P3**	B-PD:nppukp	MI2S_DATA_SD1		AUD_SBI_CLK_B						
BD32	32	►	**P3**	BH-PD:nppukp	MI2S_DATA_SD0	GP_CLK_2A							
BF32	33		**P3**	B-PD:nppukp	MI2S_MCLK	GP_PDM_2B			BOOT_CONFIG[4]				
AG5	34	►	**P3**	BH-PD:nppukp	CODEC_MIC_I2S_MCLK	GP_CLK_0B			BOOT_CONFIG[3]				
AF2	35		**P3**	B-PD:nppukp	CODEC_MIC_I2S_SCK								
AF6	36	►	**P3**	BH-PD:nppukp	CODEC_MIC_I2S_WS		USB_FS1_OE_B_N	GSBI1_SPI_CS1C_N					
AF4	37		**P3**	B-PD:nppukp	CODEC_MIC_I2S_DIN0		USB_FS1_DAT_B						
AE5	38	►	**P3**	BH-PD:nppukp	CODEC_MIC_I2S_DIN1	GP_PDM_0A	USB_FS1_SE0_B						
BA31	39	►	**P3**	BH-PD:nppukp	CODEC_SPKR_I2S_MCLK		AUD_SB1_MCLK		BOOT_CONFIG[2]				
BF40	40		**P3**	B-PD:nppukp	CODEC_SPKR_I2S_SCK		AUD_SB1_CLK_A				ETM_RIVA_TRACESYNC_B		
BE39	41	►	**P3**	BH-PD:nppukp	CODEC_SPKR_I2S_DOUT		AUD_SB1_DATA_A				ETM_RIVA_PIPESTAT_B(2)		
BE31	42	►	**P3**	BH-PD:nppukp	CODEC_SPKR_I2S_WS						ETM_RIVA_PIPESTAT_B(1)		
BF34	43		**P3**	B-PD:nppukp	AUDIO_PCM_DOUT						ETM_RIVA_PIPESTAT_B(0)		
BF36	44	►	**P3**	BH-PD:nppukp	AUDIO_PCM_DIN	GP_PDM_1B					ETM_RIVA_PIPESTAT_A(2)		
BD36	45	►	**P3**	BH-PD:nppukp	AUDIO_PCM_SYNC		VFE_CAMIF_TIMER4_A				ETM_RIVA_TRACESYNC_A		
BC35	46		**P3**	B-PD:nppukp	AUDIO_PCM_CLK		VFE_CAMIF_TIMER5_A				ETM_RIVA_TRACEPKT_A(0)		
BB30	47	►	**P3**	BH-PD:nppukp	SPKR_I2S_SCK		VFE_CAMIF_TIMER7_A						
BA29	48		**P3**	B-PD:nppukp	SPKR_I2S_WS		GSBI1_SPI_CS2C_N						
BF30	49	►	**P3**	BH-PD:nppukp	SPKR_I2S_DOUT		GP_PDM_2A				ETM_TRACE_A_DATA(11)		
BC29	50	►	**P3**	BH-PD:nppukp	SPKR_I2S_MCLK	GP_CLK_1B			BOOT_CONFIG[1]		ETM_TRACE_A_DATA(10)		
BF28	51		**P3**	B-PD:nppukp	MIC_I2S_DIN		VFE_CAMIF_TIMER6_A			GSBI5_3	ETM_TRACE_A_DATA(9)		
BD28	52	►	**P3**	BH-PD:nppukp	MIC_I2S_SCK		USB_FS1_OE_A_N			GSBI5_2	ETM_TRACE_A_DATA(8)		
BE27	53		**P3**	B-PD:nppukp	MIC_I2S_WS	{USB_UICC_DP}	USB_FS1_DAT_A			GSBI5_1	ETM_TRACE_A_DATA(7)		
BF26	54		**P3**	B-PD:nppukp	MIC_I2S_MCLK	{USB_UICC_DM}	USB_FS1_SE0_A			GSBI5_0	ETM_TRACE_A_DATA(6)		
AY2	55	►	**P3**	BH-PD:nppukp			TSIF1_CLK				ETM_TRACE_B_DATA(13)		
AY4	56	►	**P3**	BH-PD:nppukp			TSIF1_EN	GSBI1_SPI_CS3A_N			ETM_TRACE_B_DATA(12)		
AW5	57		**P3**	B-PD:nppukp	SDC2_CMD		TSIF1_DATA				ETM_TRACE_B_DATA(11)		
AW3	58	►	**P3**	BH-PD:nppukp	SDC2_DATA[3]		TSIF2_SYNC				ETM_TRACE_B_DATA(10)		
AV2	59		**P3**	B-PD:nppukp	SDC2_CLK		TSIF2_CLK				ETM_TRACE_B_DATA(9)		
AT4	60		**P3**	B-PD:nppukp	SDC2_DATA[2]		TSIF2_EN				ETM_TRACE_B_DATA(8)		
AT2	61	►	**P3**	BH-PD:nppukp	SDC2_DATA[1]		TSIF2_DATA				ETM_TRACE_B_DATA(7)		
AR3	62		**P3**	B-PD:nppukp	SDC2_DATA[0]		TSIF1_SYNC				ETM_TRACE_B_DATA(6)		
BD40	63	►	**P3**	BH-PD:nppukp	SDC4_DATA[3]	SSBI_BT							
BC39	64		**P3**	B-PD:nppukp	SDC4_DATA[2]	WLAN_DATA[2]							
BA37	65	►	**P3**	BH-PD:nppukp	SDC4_DATA[1]	WLAN_DATA[1]							
BB38	66		**P3**	B-PD:nppukp	SDC4_DATA[0]	WLAN_DATA[0]							
BD34	67		**P3**	B-PD:nppukp	SDC4_CMD	WLAN_CMD							
BC33	68		**P3**	B-PD:nppukp	SDC4_CLK	WLAN_CLK							
AD4	69		**P3**	B-PU:nppdkp	HDMI_CEC								
AC3	70		**P3**	B-PU:nppdkp	HDMI_DDC_CLOCK								
AC5	71		**P3**	B-PU:nppdkp	HDMI_DDC_DATA								
AB4	72	►	**P3**	BH-PD:nppukp	HDMI_HOT_PLUG_DETECT								
M2	73	►	**P3**	BH-PU:nppdkp		PMIC1_SEC_INT_N							
M4	74	►	**P3**	BH-PU:nppdkp		PMIC1_USR_INT_N							
L3	75	►	**P3**	BH-PU:nppdkp		PMIC_MDM_INT_N							
K2	76	►	**P3**	BH-PU:nppdkp		PMIC2_SEC_INT_N							
J5	77	►	**P3**	BH-PU:nppdkp		PMIC2_USR_INT_N							
H4	78		**P3**	B-PD:nppukp	PS_HOLD								
H2	79		**P3**	B-PD:nppukp	SSBI_PMIC2								
B34	80		**P3**	B-PD:nppukp	GPS_BLANKING								
AR5	81	►	**P3**	BH-PD:nppukp	SSBI_EXT_GPS								
D20	82		**P3**	B-PD:nppukp	PCI_E_RST_N					GSBI7_3			
B22	83	►	**P3**	BH-PD:nppukp	PCI_E_WAKE_N					GSBI7_2			
C21	84	►	**P3**	BH-PD:nppukp	PCI_E_PRSNT_2_N					GSBI7_1			
D22	85		**P3**	B-PD:nppukp	PCI_E_PWREN_N					GSBI7_0			
B24	86		**P3**	B-PD:nppukp	PCI_E_PWRFLT_N								
B20	87		**P3**	B-PD:nppukp	SATA_LED				BOOT_CONFIG[0]				
AB2	88	►	**P4**	BH-PD:nppukp	USB2_HSIC_STROBE		VFE_CAMIF_TIMER4_B						
Y4	89		**P4**	B-PD:nppukp	USB2_HSIC_DATA		VFE_CAMIF_TIMER3_B						

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